

Figure 1

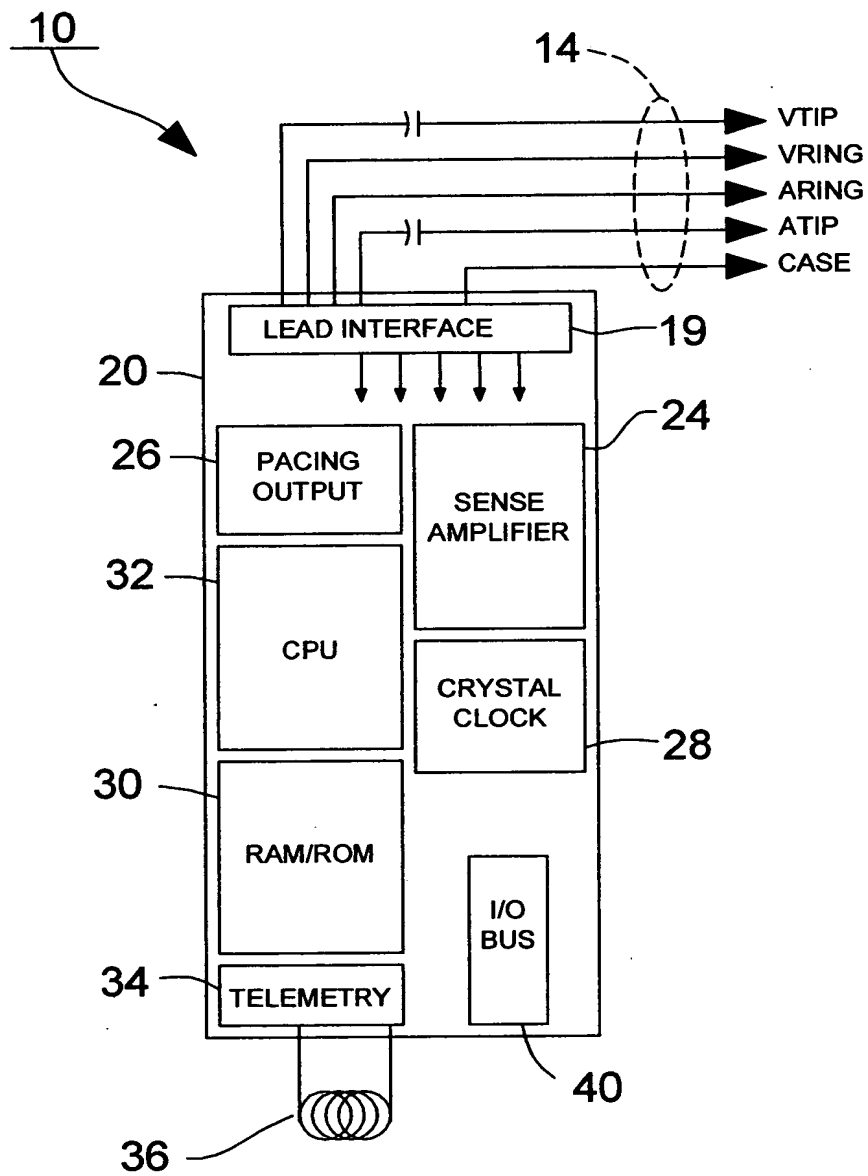


Figure 2

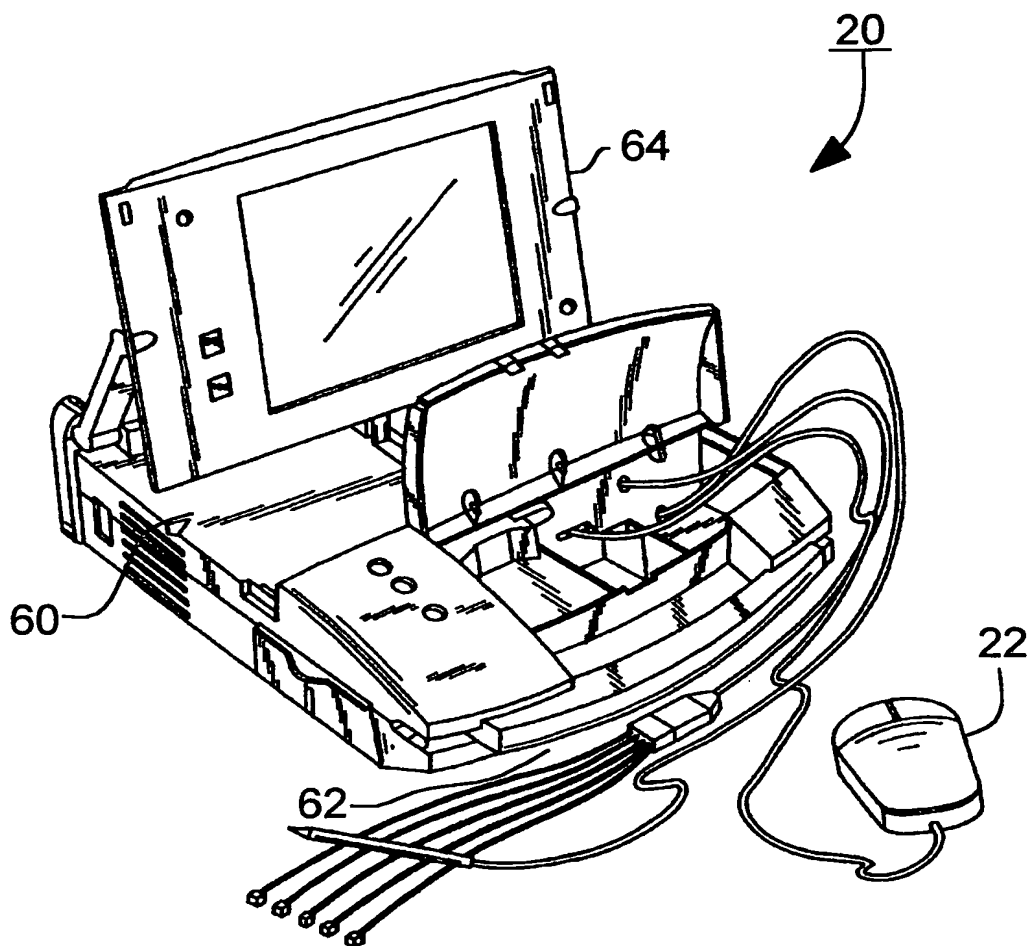


Figure 3

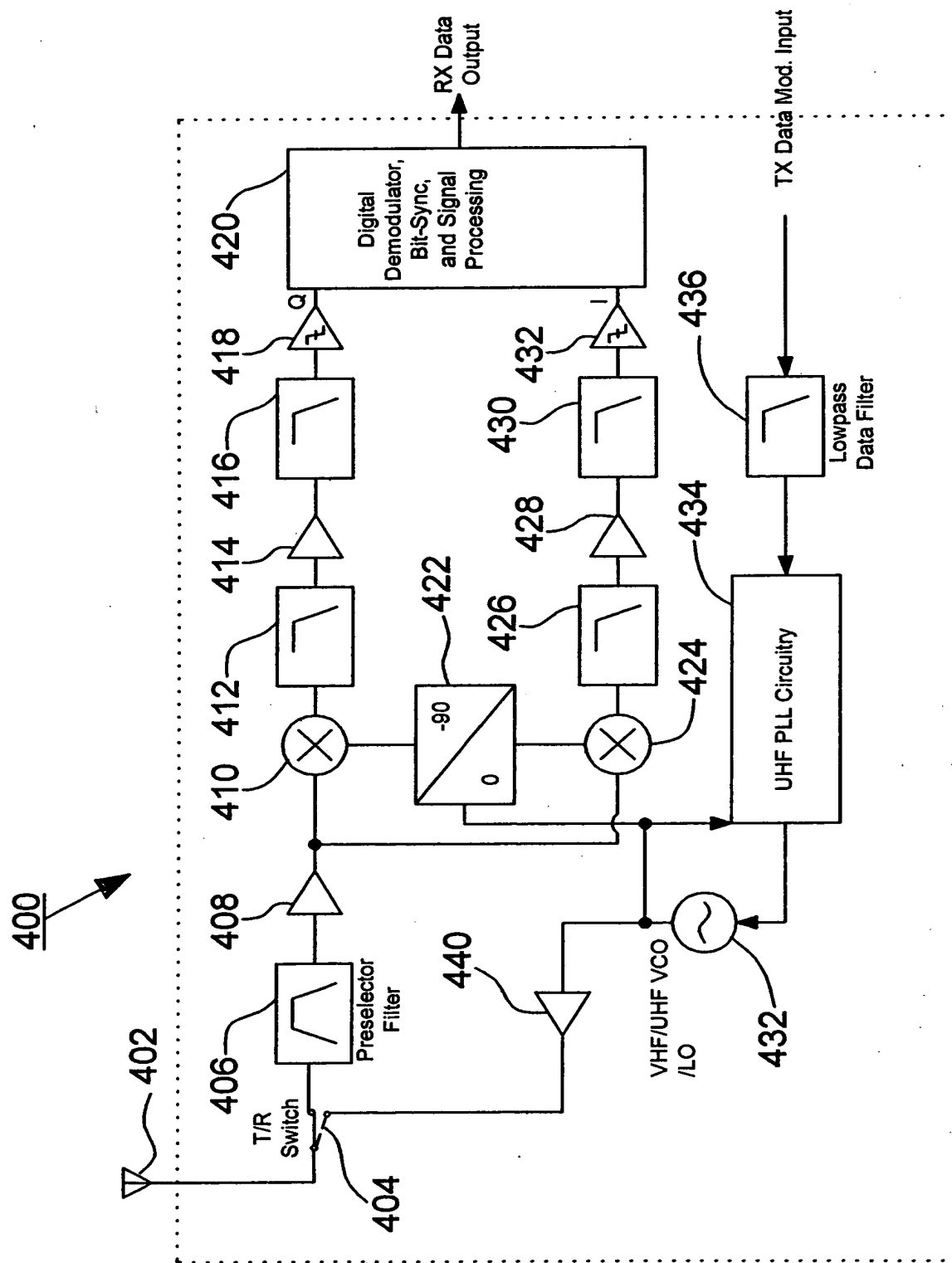


Figure 4

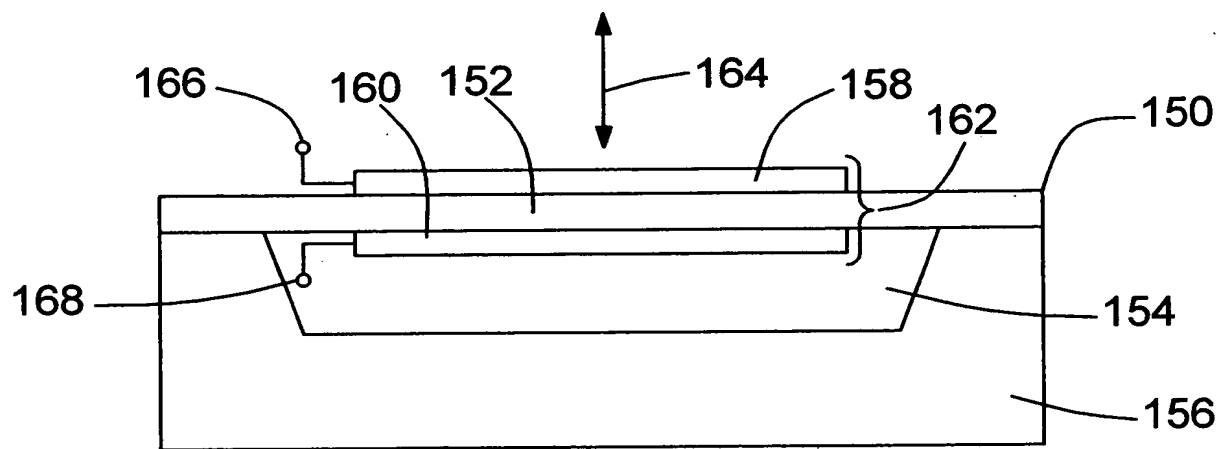


Figure 5A

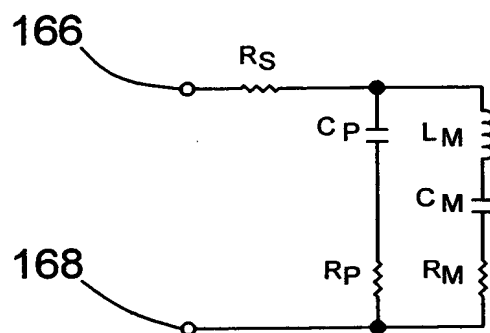


Figure 5B

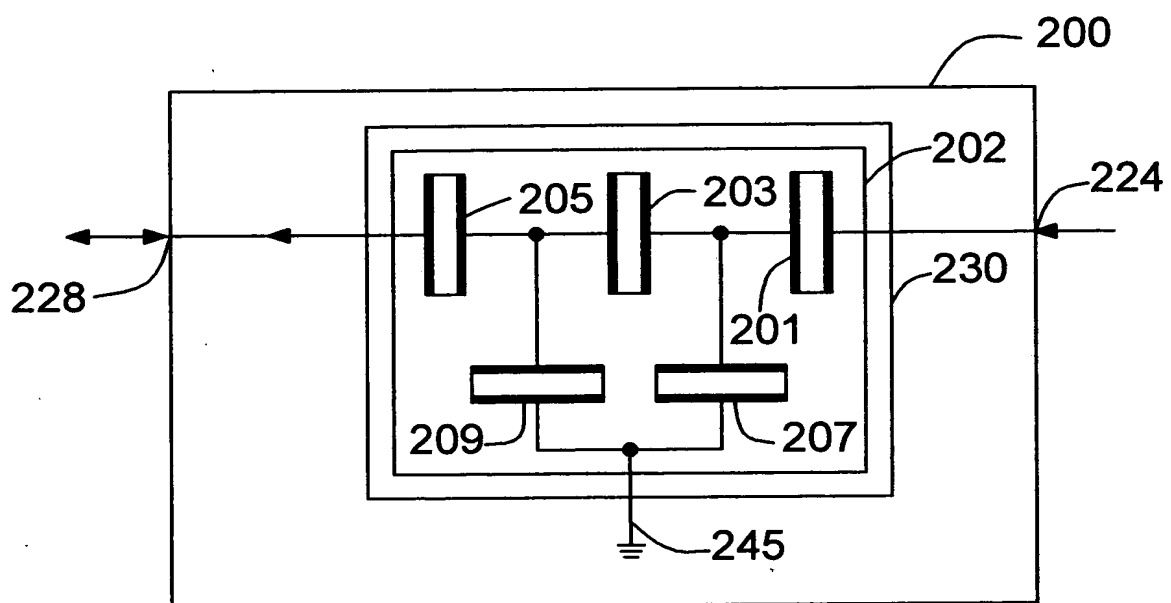


Figure 6

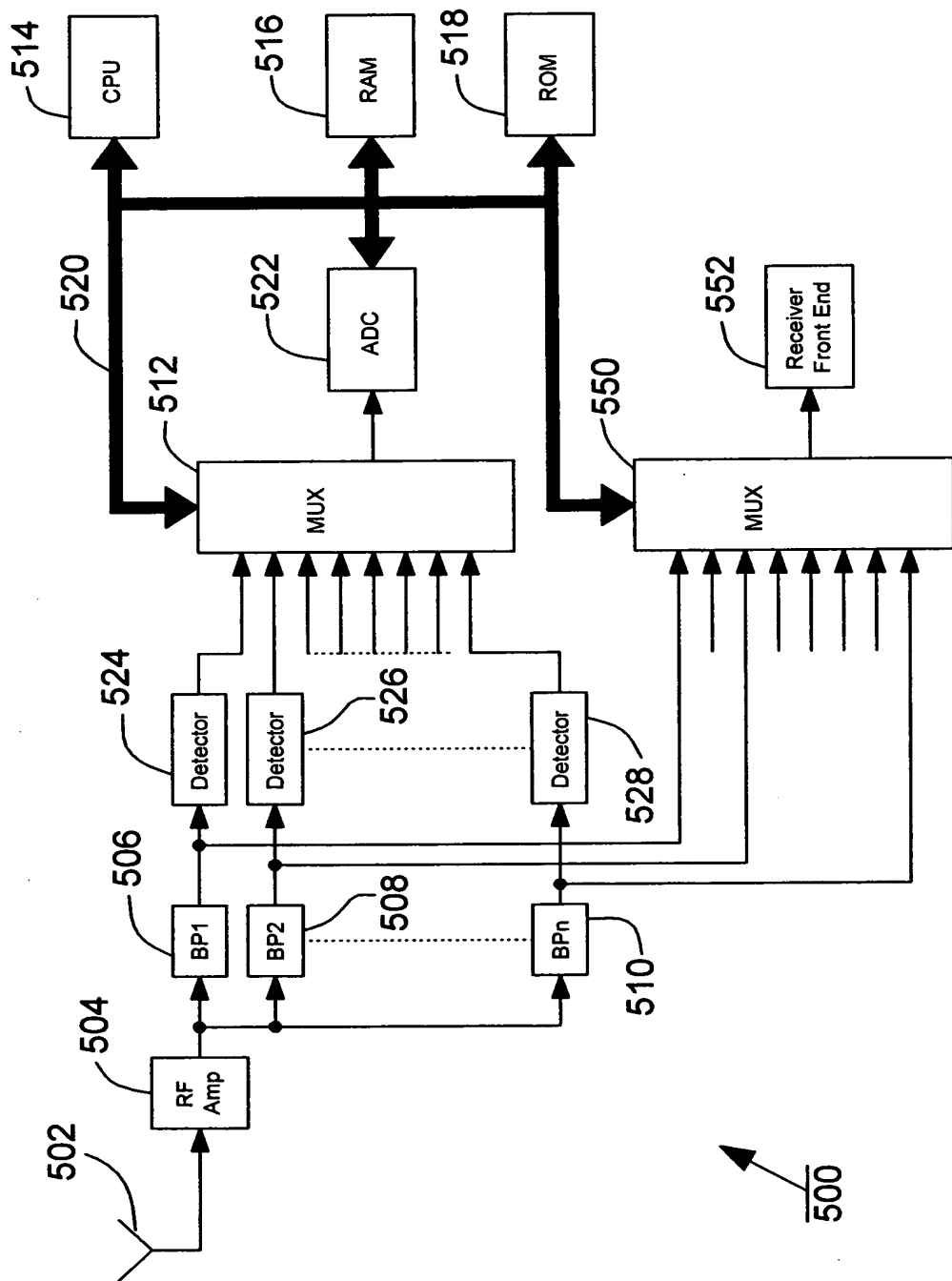


Figure 7

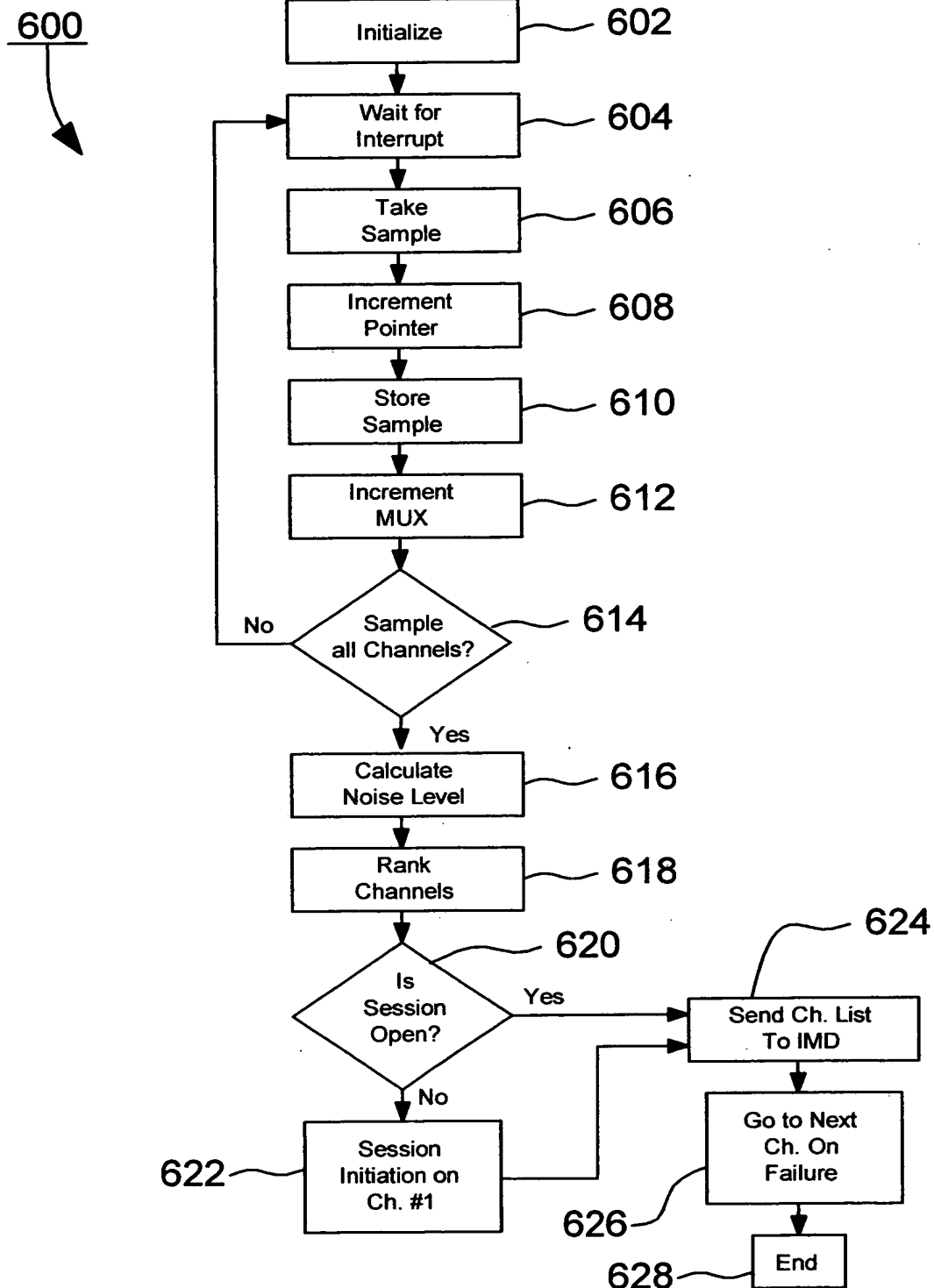


Figure 8



FIG. 9 is a block diagram of a system 500. The system 500 includes an antenna 502, a plurality of bandpass filters (BPFs) 560, 562, and 564, a multiplexer (MUX) 566, a radio frequency (RF) amplifier 570, a receiver/demodulator 572, and a programming data storage 568. The antenna 502 is connected to the BPFs 560, 562, and 564. The outputs of the BPFs 560, 562, and 564 are connected to the MUX 566. The MUX 566 is connected to the RF amplifier 570, which is connected to the receiver/demodulator 572. The receiver/demodulator 572 is connected to the programming data storage 568.

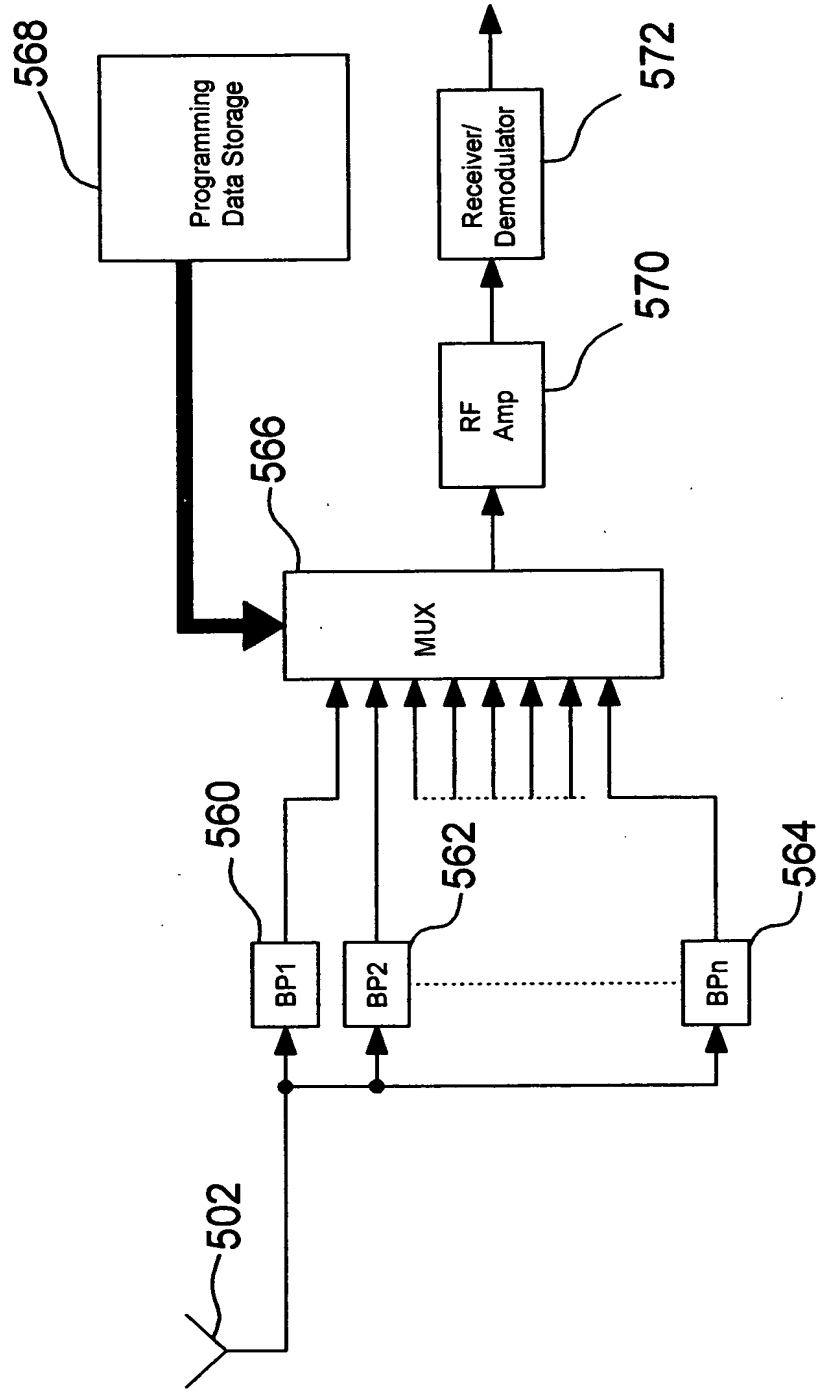


Figure 9